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SERIAL NUMBER 10/718,310	FILING DATE 11/20/2003 RULE	CLASS 257	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. MUH-12876
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APPLICANTS

Bernhard Kowalski, Munchen, GERMANY;

Andreas Felber, Munchen, GERMANY;

Valentin Rosskopf, Pottmes, GERMANY; Till Schlosser, Dresden, GERMANY;
Jurgen Lindolf, Friedberg, GERMANY;

** CONTINUING DATA *****

(None)

** FOREIGN APPLICATIONS *****

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY GERMANY	SHEETS DRAWING 4	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 2
Verified and Acknowledged Examiner's Signature	Initials				

ADDRESS

24131
LERNER AND GREENBERG, PA
P O BOX 2480
HOLLYWOOD , FL
33022-2480

TITLE

Transistor array and semiconductor memory configuration fabricated with the transistor array

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